



DMT3006LFDF

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
	$7m\Omega @ V_{GS} = 10V$	14.1A
30V	10mΩ @ V <sub>GS</sub> = 4.5V	11.8A
	15mΩ @ $V_{GS}$ = 3.7V	9.6A

## Description

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

# Applications

- Battery Management Application
- Power Management Functions
- DC-DC Converters

#### Features

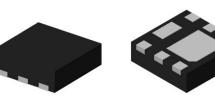
- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm<sup>2</sup>
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

N-CHANNEL ENHANCEMENT MODE MOSFET

- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

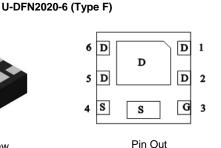
## **Mechanical Data**

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.0065 grams (Approximate)

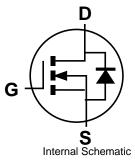


Top View

Bottom View







#### Ordering Information (Note 4)

Part Number	Reel Size (inches)	Quantity per Reel
DMT3006LFDF-7	7	3,000
DMT3006LFDF-13	13	10,000

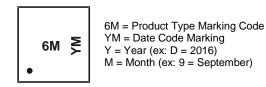
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



Date Code Key

Date Obde Rey												
Year	201	5	2016		2017	20	18	2019		2020	2	2021
Code	С		D		E		F	G		Н		
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



#### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	30	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) $V_{GS}$ = 10V	ID	14.1 12.5	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	80	A
Continuous Source-Drain Diode Current (Note 6)	I <sub>S</sub>	2	A
Avalanche Current (Note 7) L = 0.1mH	I <sub>AS</sub>	25	A
Avalanche Energy (Note 7) L = 0.1mH	E <sub>AS</sub>	31	mJ

# **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>0JA</sub>	155	°C/W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	PD	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	60	°C/W
Thermal Resistance, Junction to Case (Note 6)	T <sub>C</sub> = +25°C	R <sub>0JC</sub>	6.9	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)						·	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current (T <sub>J</sub> = +25°C)	IDSS	—	_	1	μA	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	-	±100	nA	$V_{GS} = +20V, V_{DS} = 0V$ $V_{GS} = -16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)		•					
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.0	_	3.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
			5.8	7		$V_{GS} = 10V, I_{D} = 9A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	7.8	10	mΩ	$V_{GS} = 4.5V, I_D = 8A$	
			9.3	15		$V_{GS} = 3.7V, I_{D} = 5A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1.0	V	$V_{GS} = 0V, I_{S} = 2A$	
DYNAMIC CHARACTERISTICS (Note 9)						•	
Input Capacitance	Ciss	—	1,320	_			
Output Capacitance	Coss	—	490	—	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	77	—			
Gate Resistance	R <sub>G</sub>	_	1.6	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>G</sub>	_	10.6	—			
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>G</sub>	_	22.6	—	nC		
Gate-Source Charge	Q <sub>GS</sub>	_	3.5	—	nc	$V_{DD} = 15V, I_D = 9A$	
Gate-Drain Charge	Q <sub>GD</sub>	—	3.5	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	—	2.7	_			
Turn-On Rise Time	t <sub>R</sub>	—	2.7	_		$V_{DD} = 15V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	13.7	_	ns	$R_G = 3\Omega$ , $I_D = 9A$	
Turn-Off Fall Time	t <sub>F</sub>	—	5.5	_			
Reverse Recovery Time	t <sub>RR</sub>	_	10.5		ns		
Reverse Recovery Charge	Q <sub>RR</sub>	—	21.1	—	nC	I <sub>F</sub> = 1.5A, di/dt = 100A/μs	

Notes: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

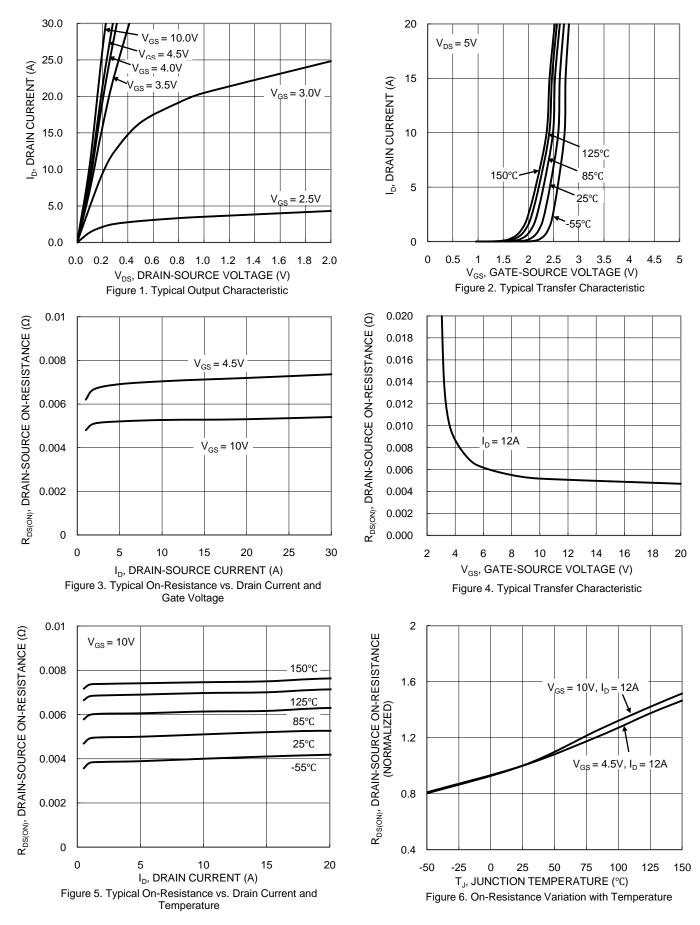
7. I<sub>AS</sub> and E<sub>AS</sub> ratings are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$ .

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.



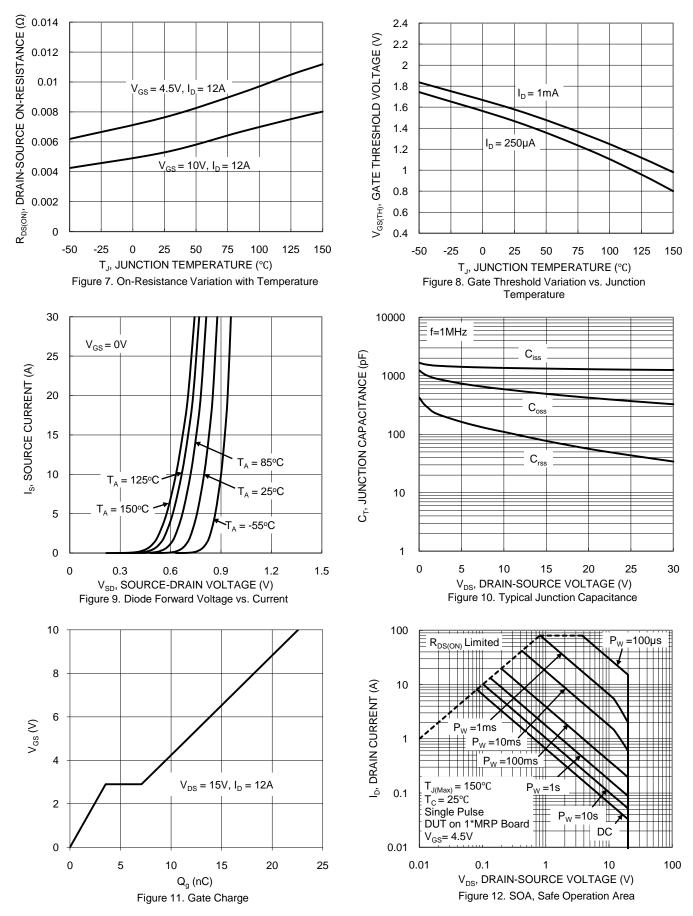
# DMT3006LFDF



DMT3006LFDF Datasheet number: DS38250 Rev. 3 - 2 3 of 7 www.diodes.com

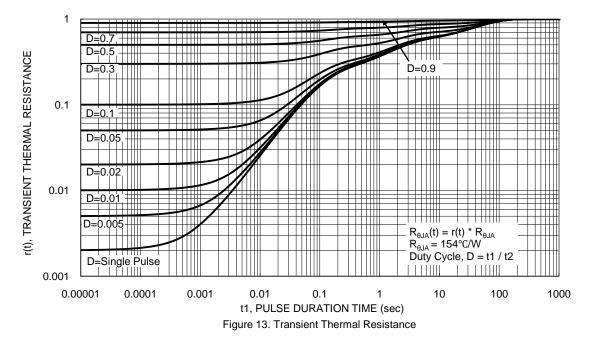


# DMT3006LFDF



DMT3006LFDF Datasheet number: DS38250 Rev. 3 - 2



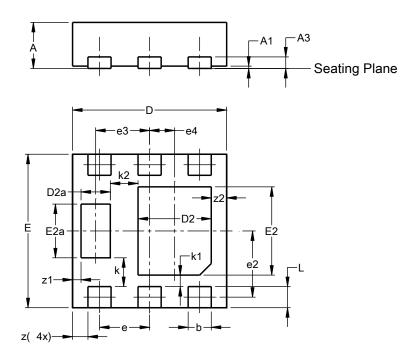




# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### U-DFN2020-6 (Type F)



U-DFN2020-6									
	(Type F)								
Dim	Min	Max	Тур						
Α	0.57	0.63	0.60						
A1	0.00	0.05	0.03						
A3	-	-	0.15						
b	0.25	0.35	0.30						
D	1.95	2.05	2.00						
D2	0.85	1.05	0.95						
D2a	0.33	0.43	0.38						
E	1.95	2.05	2.00						
E2	1.05	1.25	1.15						
E2a	0.65	0.75	0.70						
е	0.65 BSC								
e2	0.863 BSC								
e3	ł	0.70 BSC							
e4	C	).325 BS	SC						
k	0.37 BSC								
k1	0.15 BSC								
k2	0.36 BSC								
L	0.225 0.325 0.275								
z		0.20 BS							
z1	C	).110 BS	SC						
z2	0.20 BSC								
All D	Dimens	ions in	mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

# $Y_{3} Y_{2}$ Pin1 Pin1 Pin2 $Y_{3} Y_{2}$ $Y_{4}$ $Y_{7}$ $Y_{1} Y_{4}$ $Y_{1} Y_{4}$ $Y_{1} Y_{4}$ $Y_{1} Y_{4}$ $Y_{1} Y_{4}$ $Y_{1} Y_{4}$

Dimensions	Value			
Dimensions	(in mm)			
С	0.650			
Х	0.400			
X1	0.480			
X2	0.950			
X3	1.700			
Ý	0.425			
Y1	0.800			
Y2	1.150			
Y3	1.450			
Y4	2.300			

U-DFN2020-6 (Type F)



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